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APPLICANT: Rajeev Bharadhwaj JUL 23 2004

APPL. NO.: 09/992,736

CONF. NO.: 1539

FILING DATE: November 13, 2001

TITLE: System and Method for Multi-Level Memory Domain Protection

EXAMINER: Lewis Alexander Bullock, Jr.

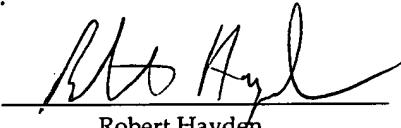
ART UNIT: 2126

ATTY. DKT. NO: PA2314US

Technology Center 2100

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I hereby certify that this paper is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450, on July 14, 2004.



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07/21/2004 HAL111 0000003 09992736

01 FC:1806

180.00 OP Information Disclosure Statement

SIR:

Pursuant to the provisions of 37 C.F.R. §§ 1.56 and 1.97-98 of the Rules of Practice in Patent Cases, enclosed herewith is form PTO-SB-08 listing several references. Copies of these references are not enclosed pursuant to 37 CFR § 1.98(d). These references have been previously cited in an application properly identified and relied upon for an earlier filing date under 35 U.S.C. § 120.

The Examiner is requested to make these references of official record in the application. The references cited may be material to examination of the application and are submitted in compliance with Applicants' duty of disclosure as defined by 37 C.F.R. § 1.56.

Applicants submit this statement pursuant to 37 C.F.R. § 1.97(c)(2) in that a first action has been mailed, prosecution has not closed and the requisite fee under 37 C.F.R. § 1.17(p) is enclosed.

No representation is made or intended as to the completeness of this list, nor is the inclusion of any reference on this list an admission that it is prior art or pertinent to this application.

Respectfully submitted,
Rajeev Bharadhwaj

July 14, 2004

By:


Robert Hayden, Reg. No. 42,645
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PTO/SB/08A (08-03)

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(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known	
Application Number	09/992,736
Filing Date	November 13, 2001
First Named Inventor	Bharadhwaj
Art Unit	2126
Examiner Name	Bullock, Jr.
Attorney Docket Number	PA2314US

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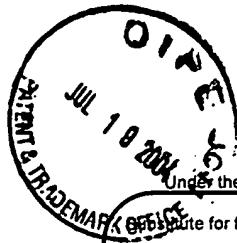
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY		
		EP 0 480 546 A2	4-15-1992	Dig. Equip. Corp.	

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				Application Number	09/992,736
				Filing Date	November 13, 2001
				First Named Inventor	Bharadhwaj
				Art Unit	2126
				Examiner Name	Bullock, Jr.
Sheet	2	of	2	Attorney Docket Number	PA2314US

NON-PATENT LITERATURE DOCUMENTS

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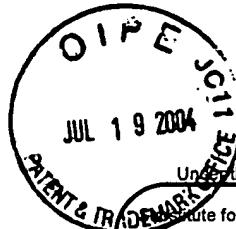
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear
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Sheet	2	of	2	Attorney Docket Number	PA2314US

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"Memory Protection Software Facility for OS/2 Shared Data Applications," IBM Technical Disclosure Bulletin, Sept. 1991, pp. 81-89, Vol. 34, No. 4A.	
		H.M. Deitel, et al. "The Design of OS/2," Dec. 30, 1992, pp. 2.3.3, 2.3.11, Addison-Wesley Publishing Co.	
		M.S. Kogan, et al. "The Design of Operating System/2," IBM Systems Journal, Jan. 1988, pp. 90-104, Vol. 27, No. 2.	
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